

[illegible][illegible][illegible][illegible][illegible][illegible][illegible][illegible][illegible]

**Model Name:GA-F2A58M-DS2H**

## Component value change history


**Version: 3.0**  
**Tip/Top : 9MF258D2H-00**  
**P-Code: U14003-0**

[illegible]

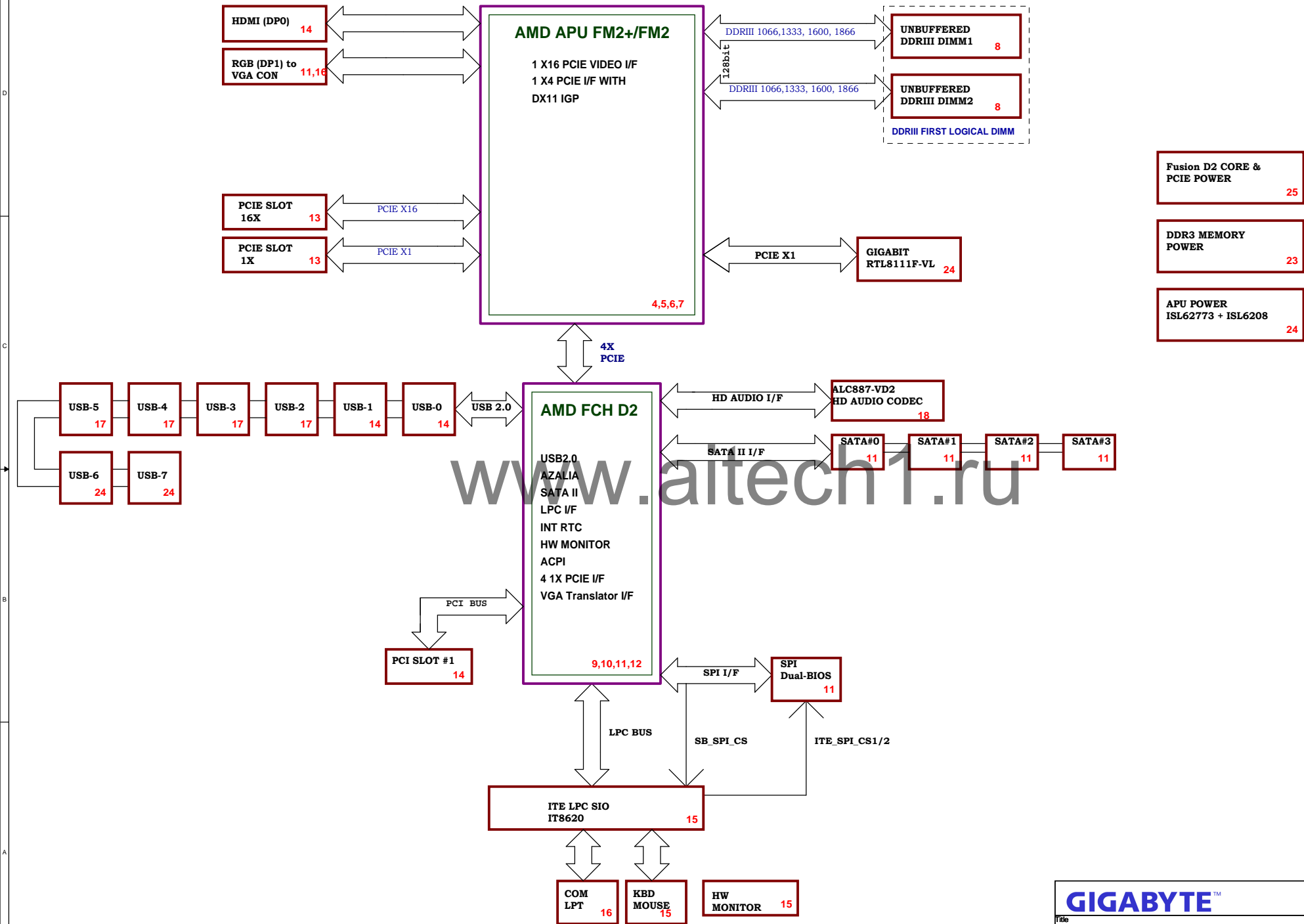
### Circuit or PCB layout change for next version

[illegible]

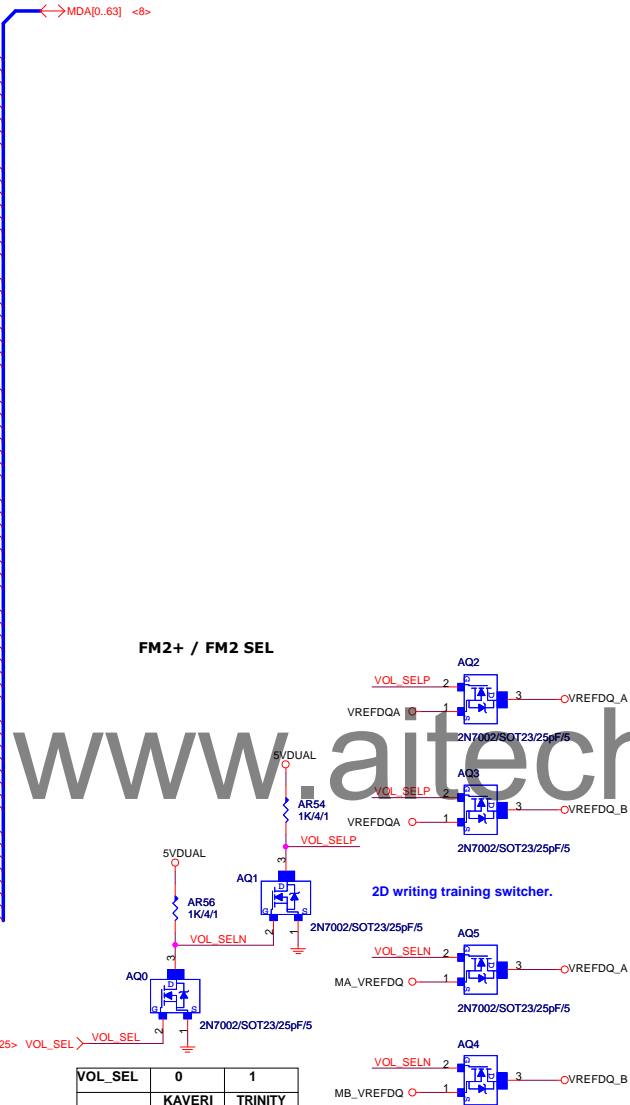
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Title			
<b>BOM &amp; PCB HISTORY</b>			
Size	Document Number	Rev	
Custom	<b>GA-F2A58M-DS2H</b>	<b>3.0</b>	
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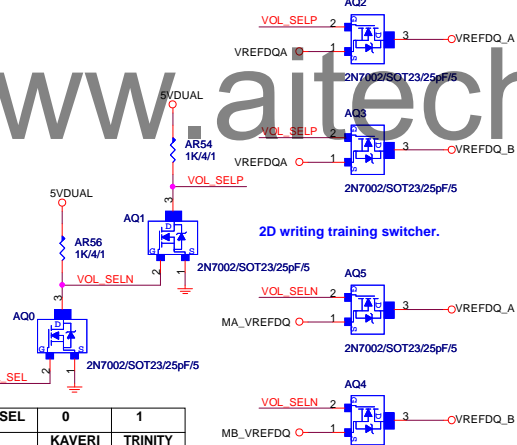
# AMD Fusion Hudson CUSTOMER DESKTOP REFERENCE DESIGN



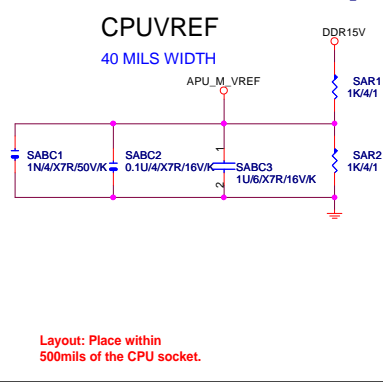
GIGABYTE™			
BLOCK DIAGRAM			
Title	BLOCK DIAGRAM		
Size	Document Number	Rev	
Custom	GA-F2A58M-DS2H	3.0	
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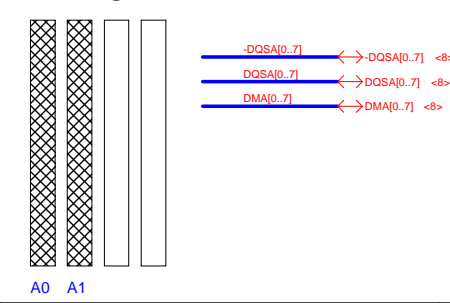
FM2+ / FM2 SEL



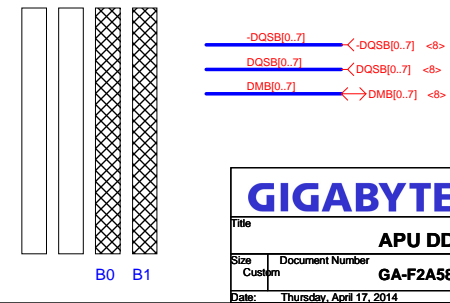
VOL_SEL	0	1
	KAVERI (FM2R2)	TRINITY (FM2)



MEM CHA



MEM CHB

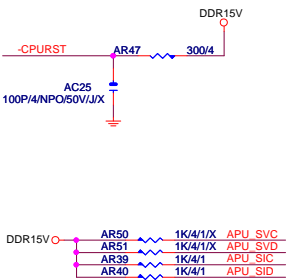


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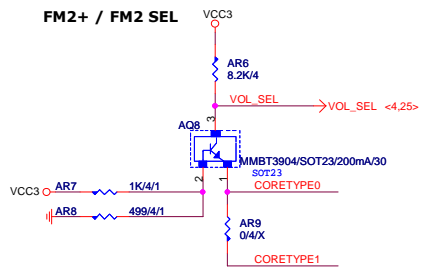
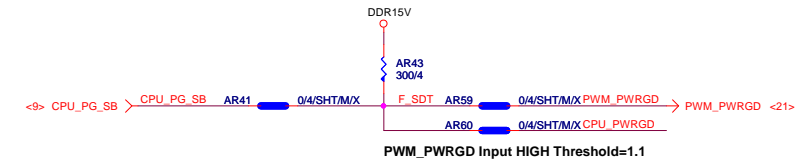
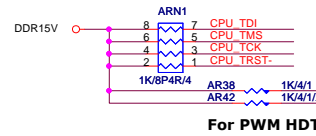
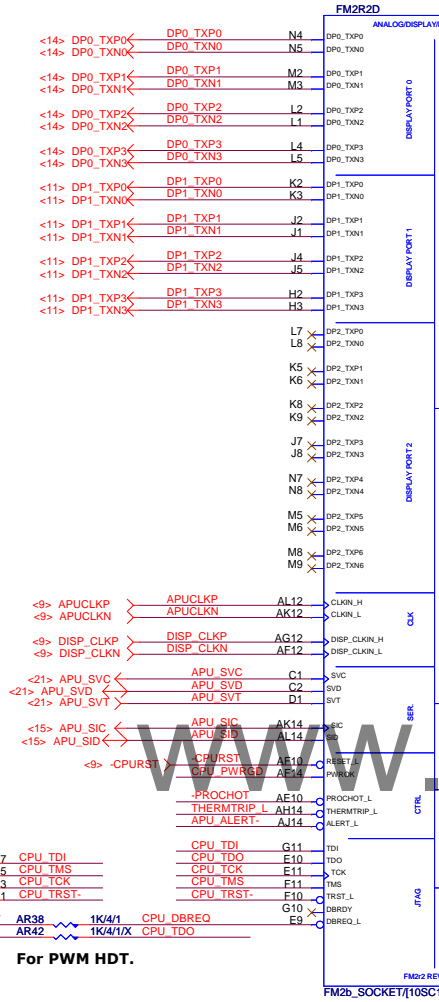
Title: **APU DDRIII MEMORY**

Size	Document Number	Rev
Custom	<b>GA-F2A58M-DS2H</b>	<b>3.0</b>

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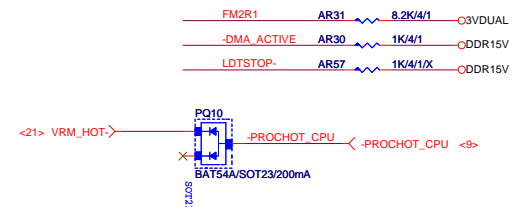
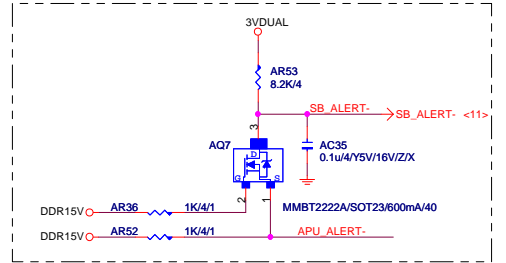
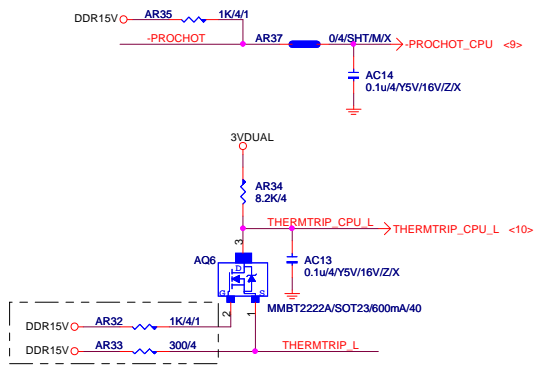


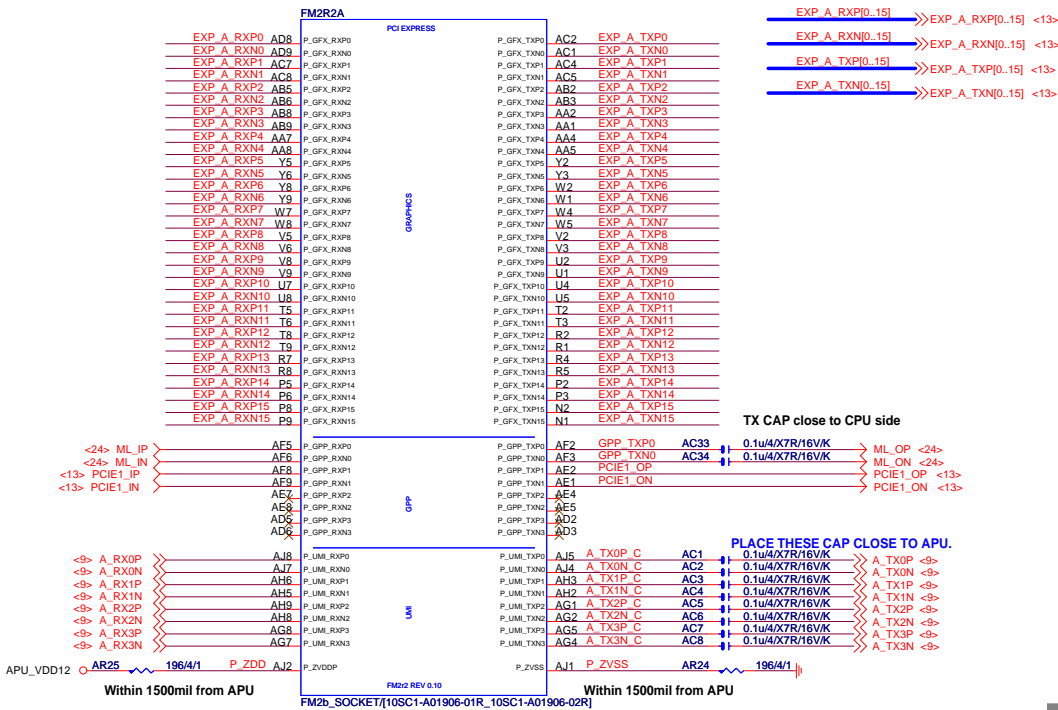
SVI 2.0 max frequency=20MHz.  
Resolution=6.5mV  
SVT=Serial VID Telemetry for  
APU get PWM information used.



APU	CORETYPE0	CORETYPE1
TRINITY	1	1
KAVERI	0	1
TBD	1	0
TBD	0	0

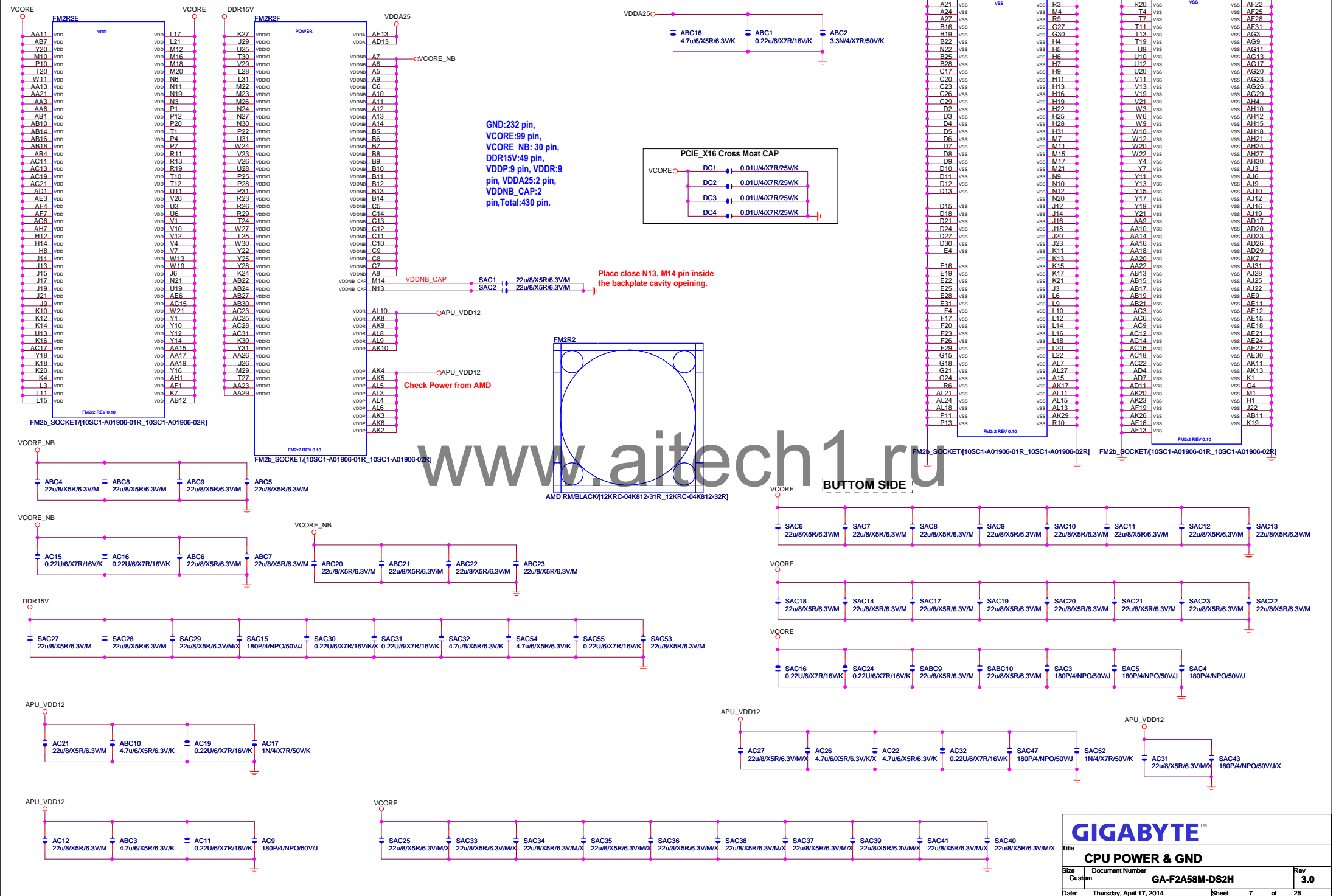
VOL_SEL	0	1
	KAVERI (FM2R2)	TRINITY (FM2)

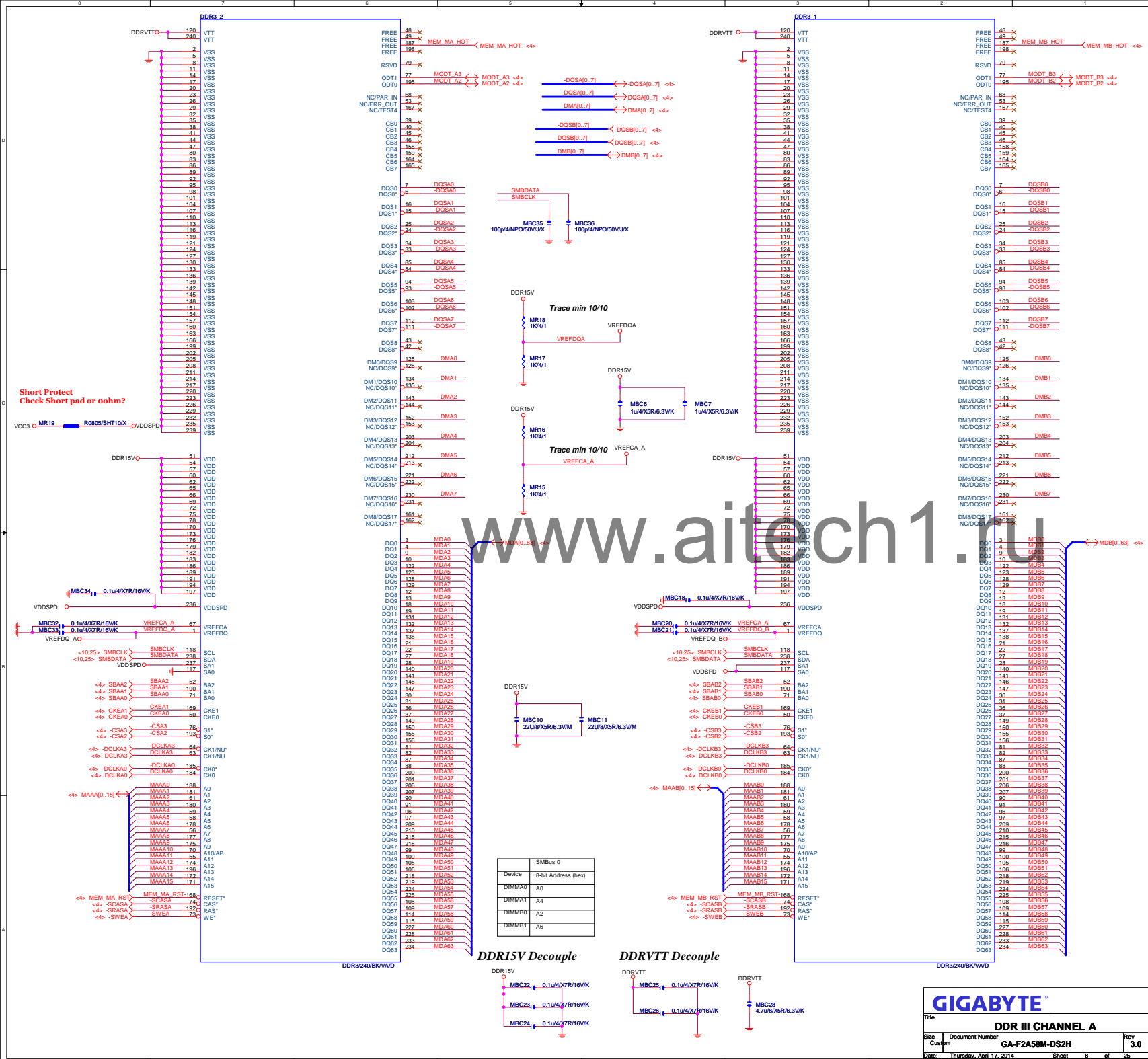




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DDR15V=1.25V/1.35V/1.5V(DDR3)

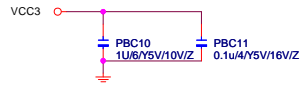






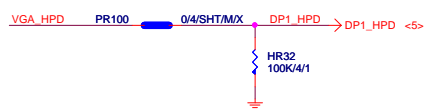
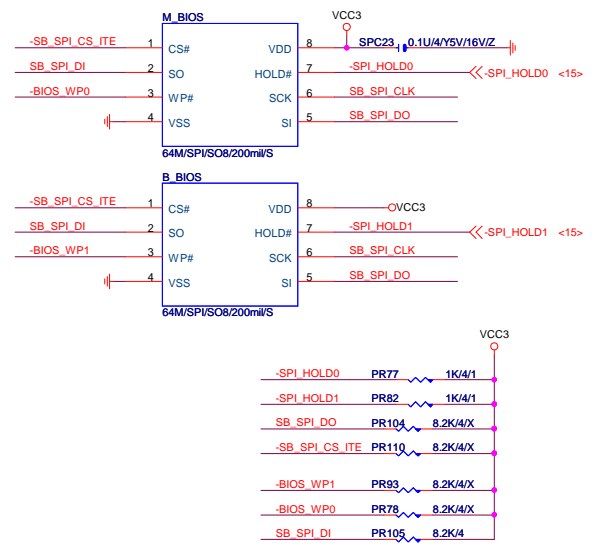
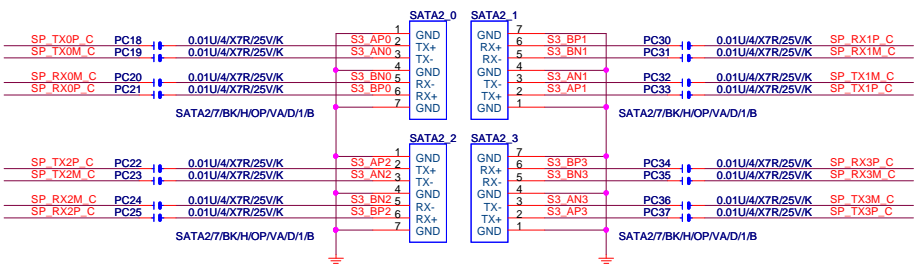
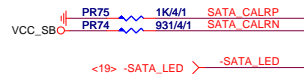






SATA 6~7 for Hudson D4.

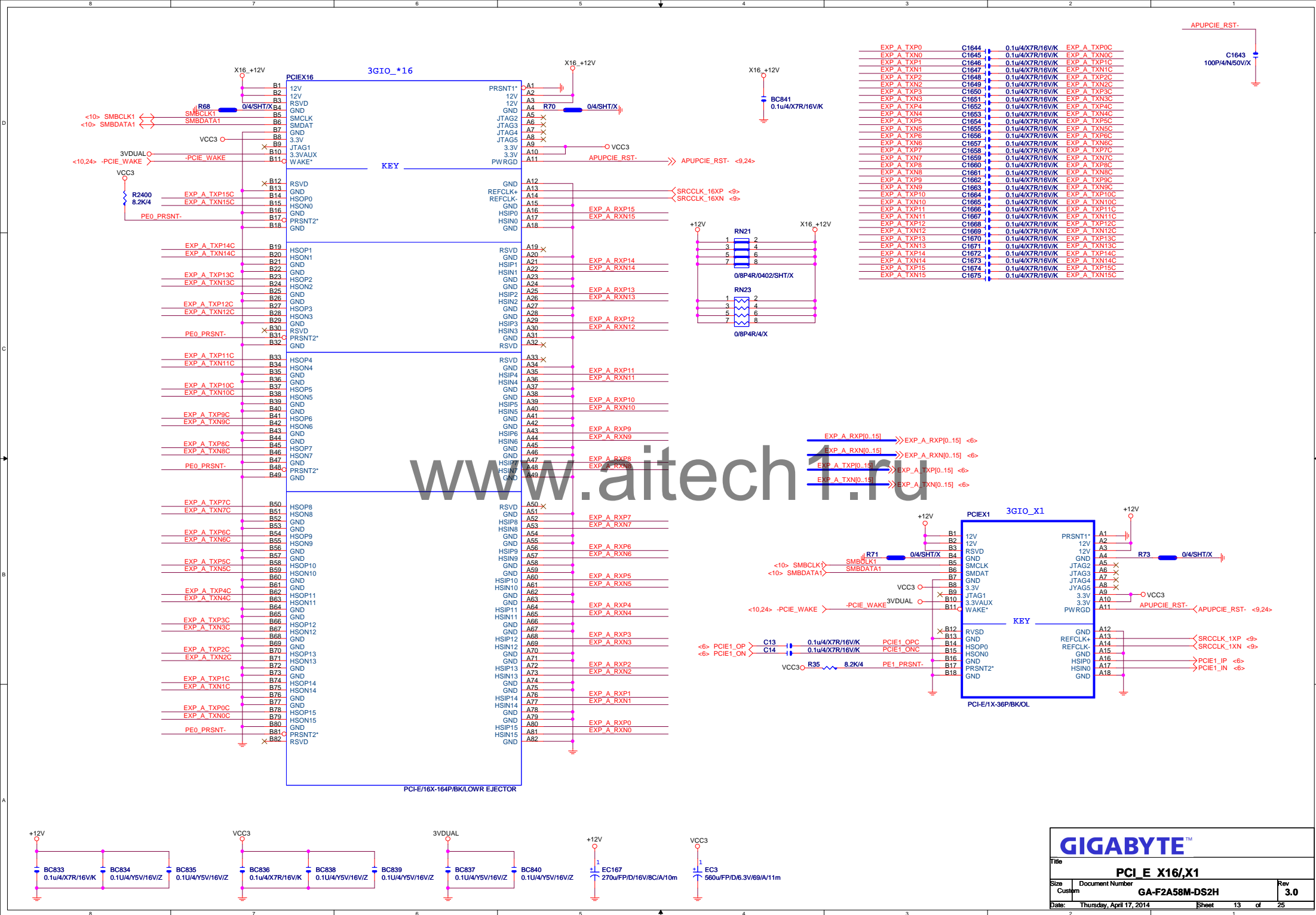
PLACE SATA\_CAL RES VERY CLOSE TO BALL OF U1



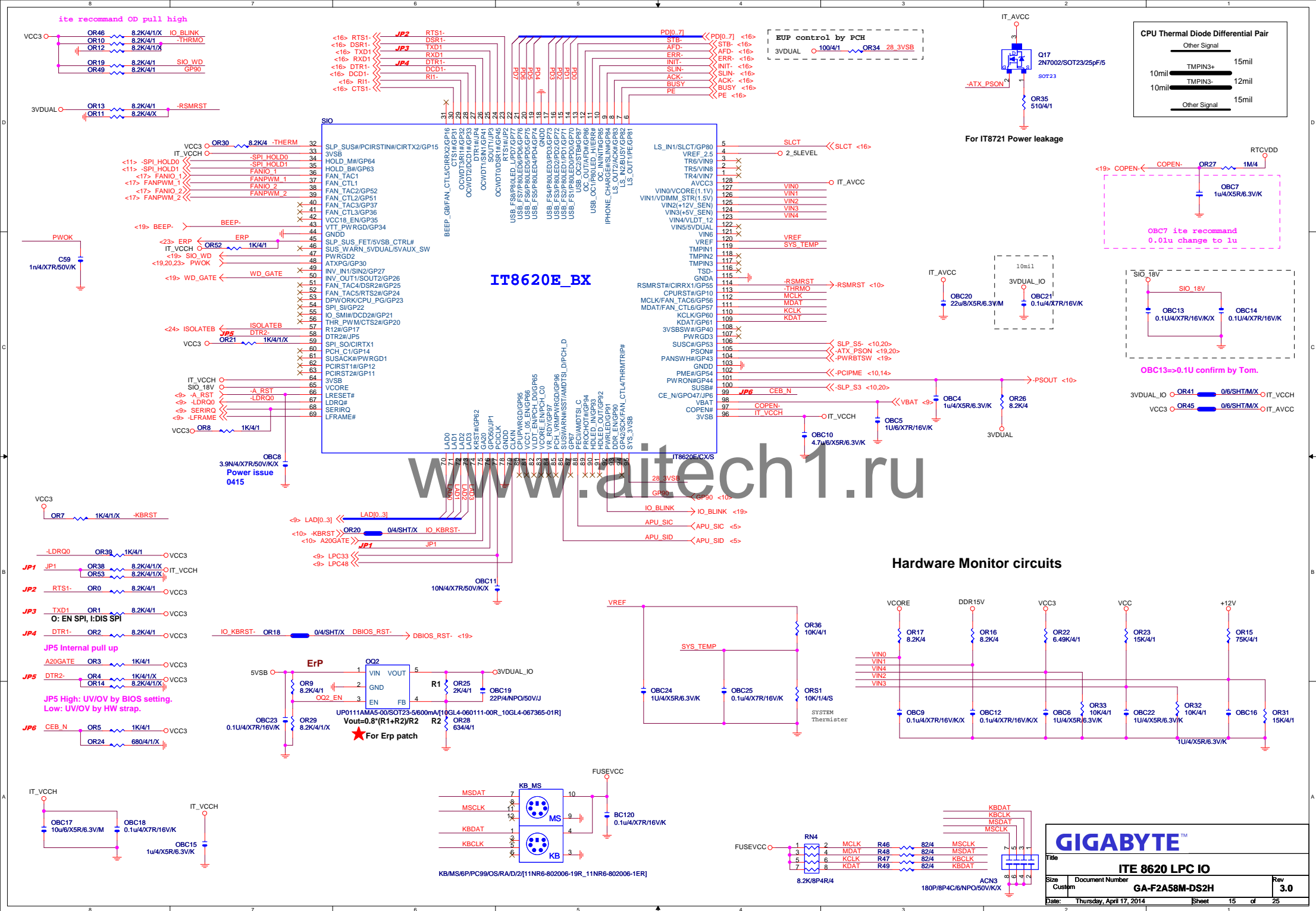
**GIGABYTE**<sup>TM</sup>

Title <b>HUDSON SATA,SPI,HWM</b>		
Size Custom	Document Number <b>GA-F2A58M-DS2H</b>	Rev <b>3.0</b>
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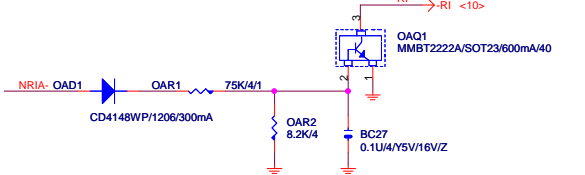
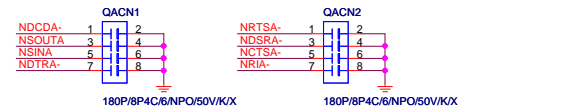
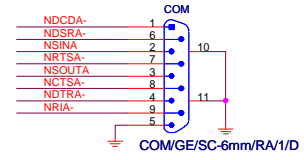
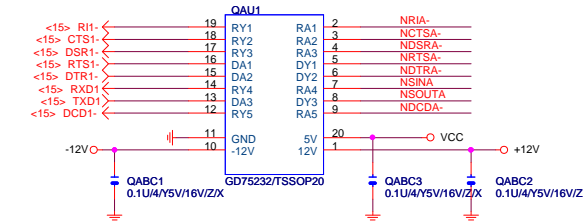




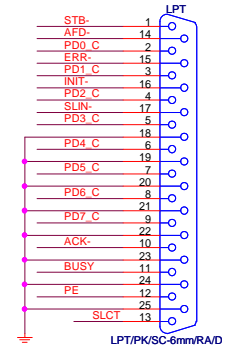
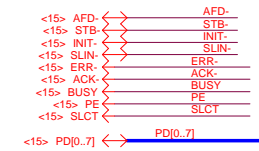
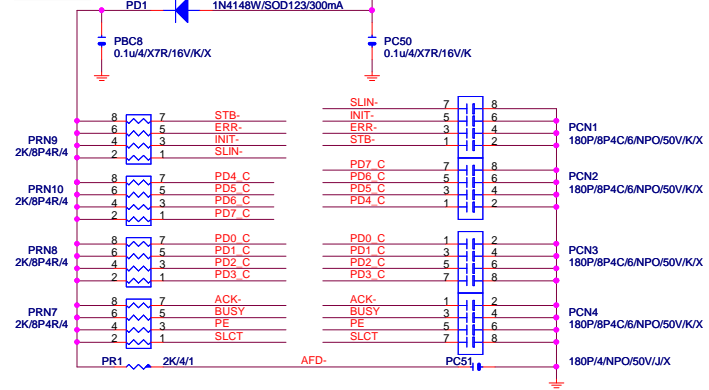
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ITE 8620 LPC IO			
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Custom	GA-F2A58M-DS2H	3.0	
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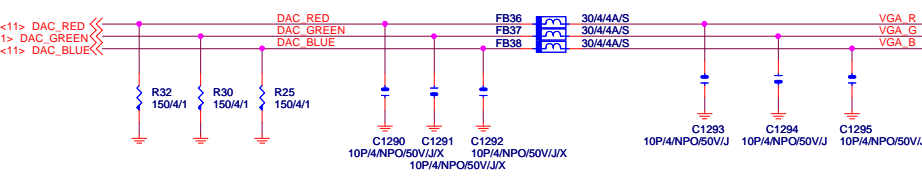
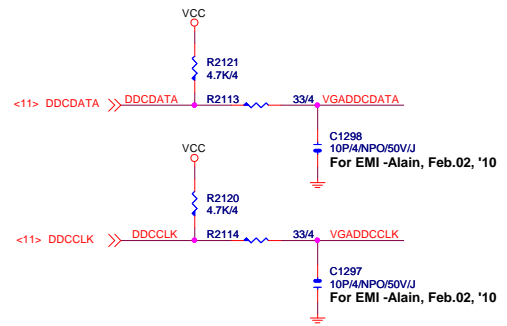
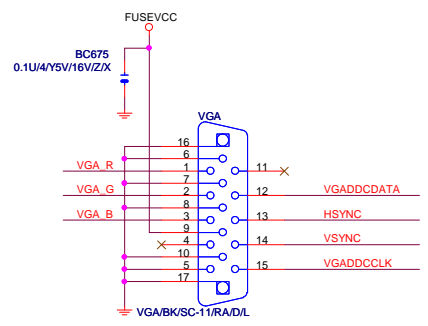
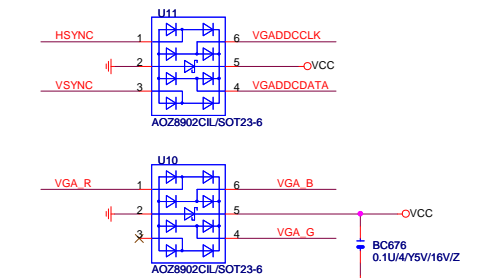
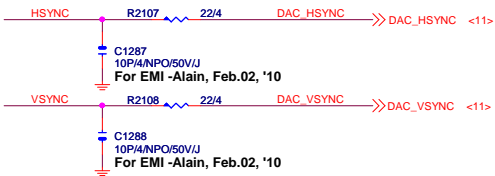




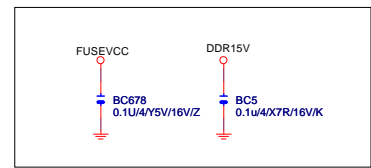
LPT



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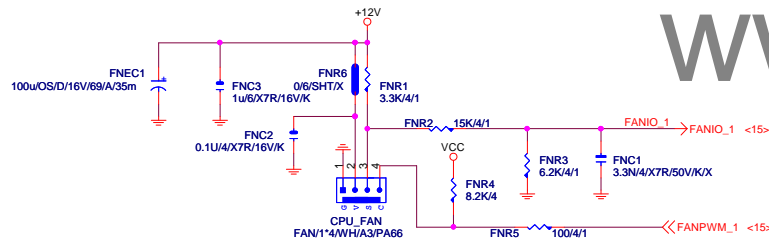


check location





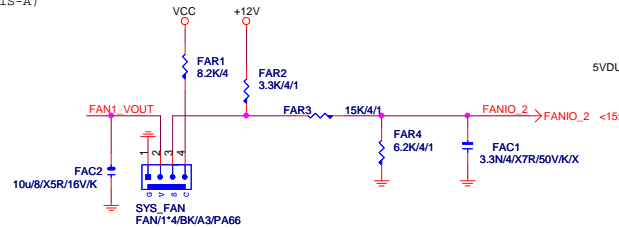
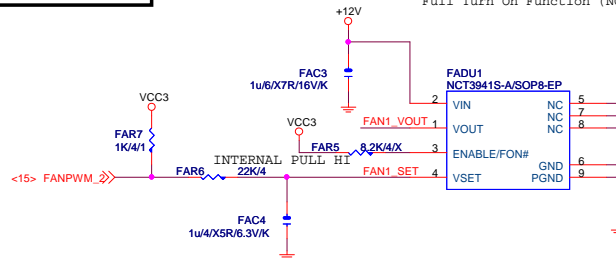
# CPU FAN



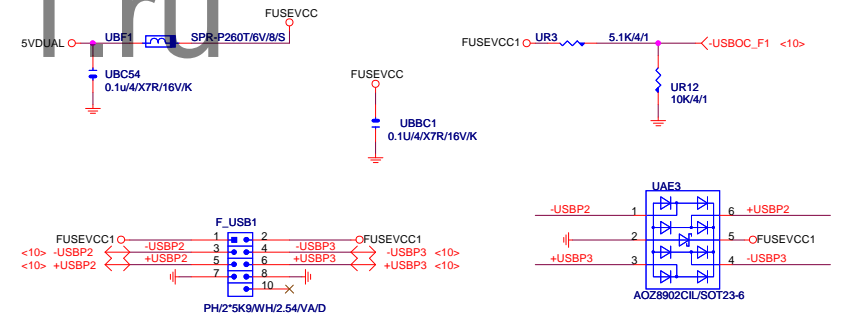
# SYSTEM FAN

## Linear SYS\_FAN

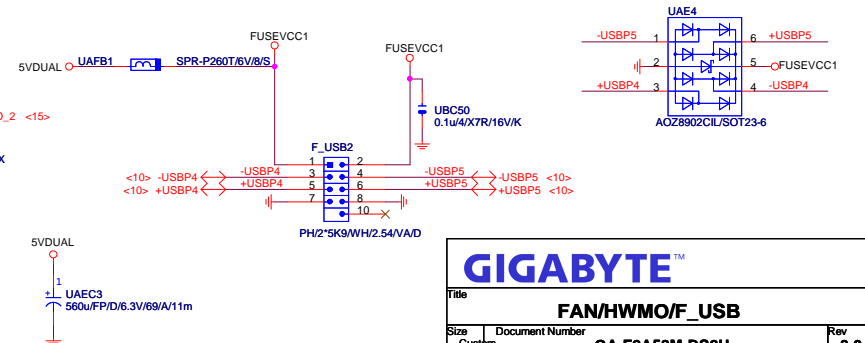
Enable Function (NCT3941S)  
Full Turn On Function (NCT3941S-A)



# FRONT SIDE USB1

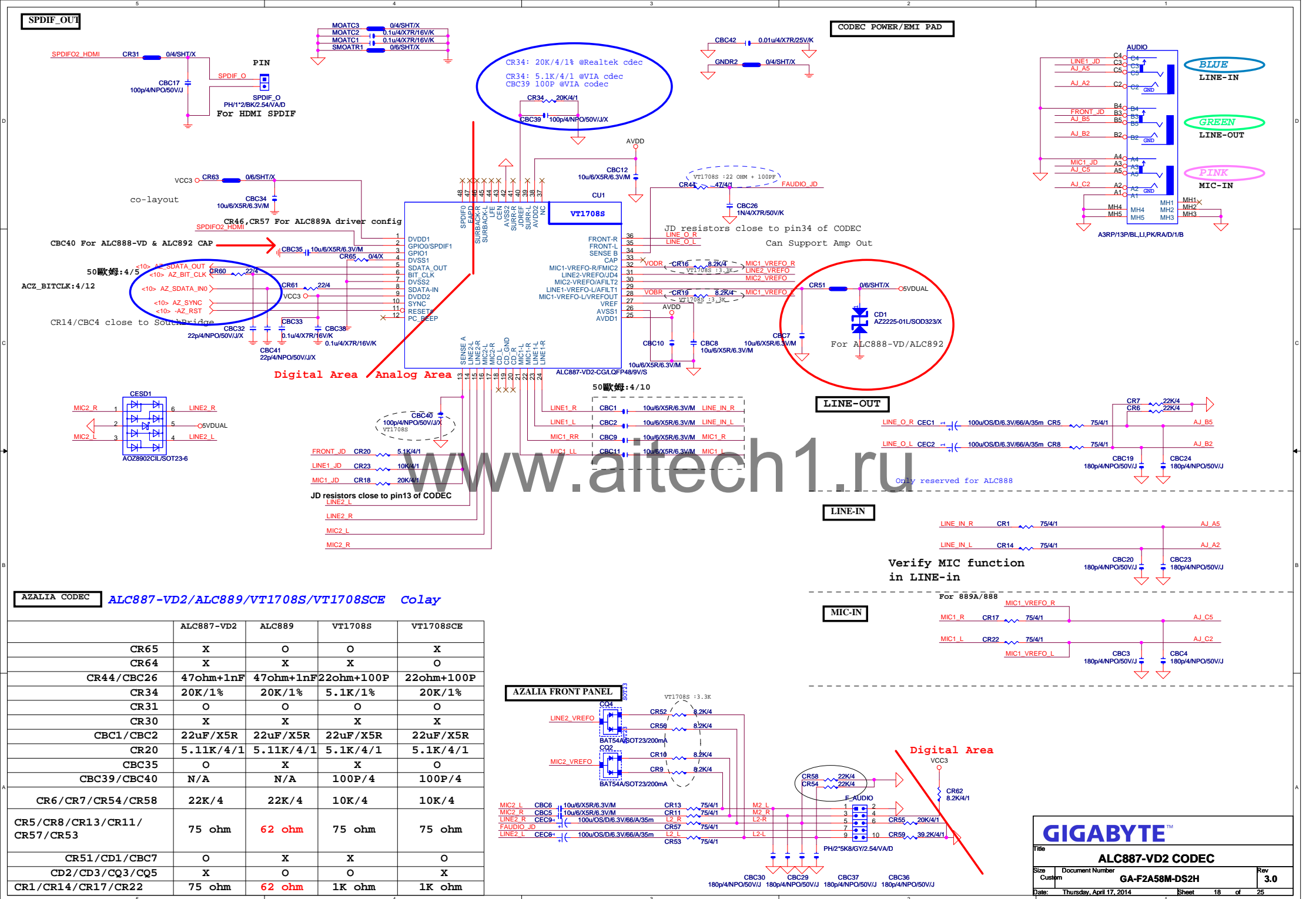


# FRONT SIDE USB2



**GIGABYTE™**

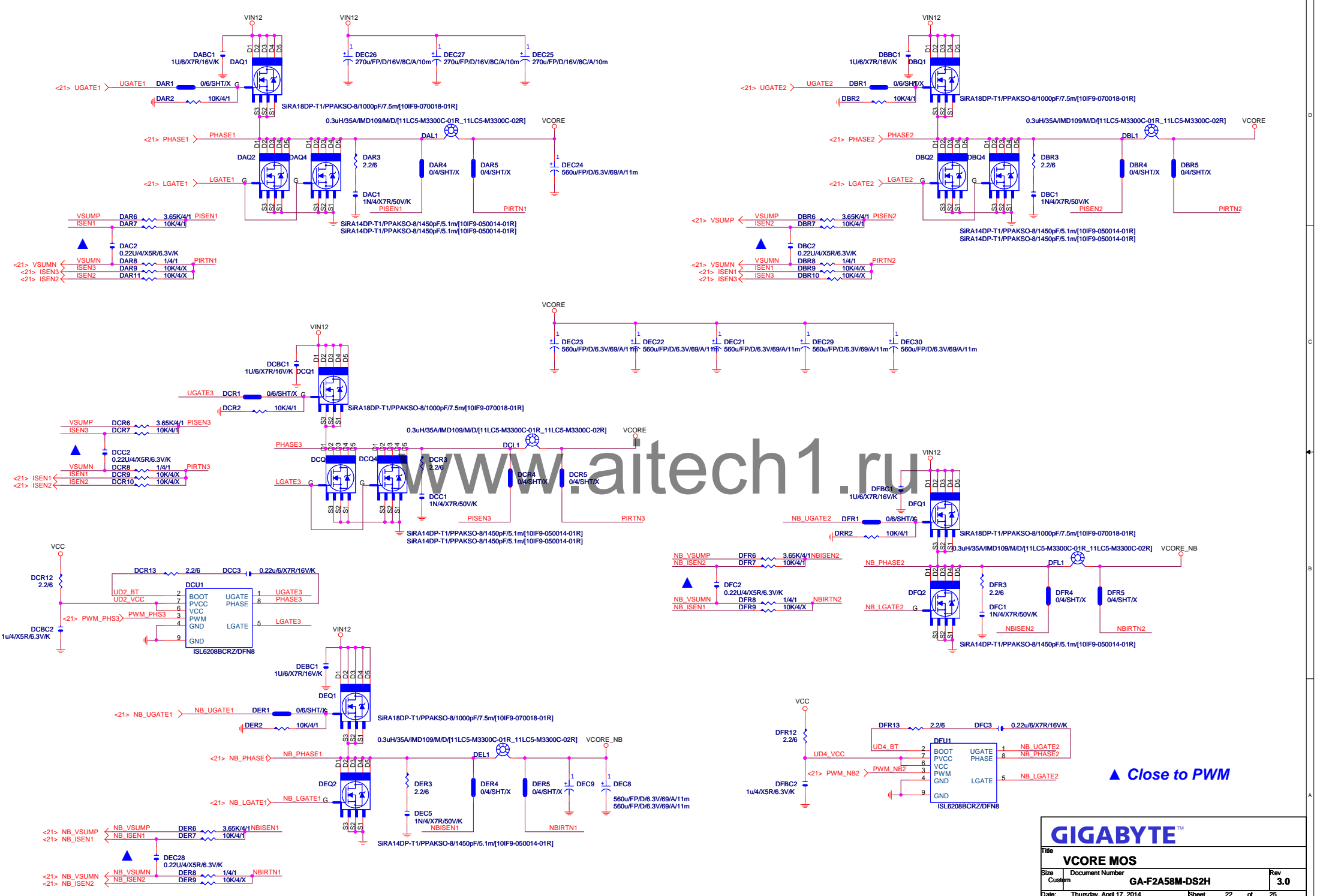
Title			FAN/HWMO/F_USB
Size	Document Number	Rev	
Custom	GA-F2A58M-DS2H	3.0	
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▲ Close to PWM

**GIGABYTE™**

Title

VCORE MOS

Size

Custom

Document Number

GA-F2A58M-DS2H

Rev

3.0

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Thursday, April 17, 2014

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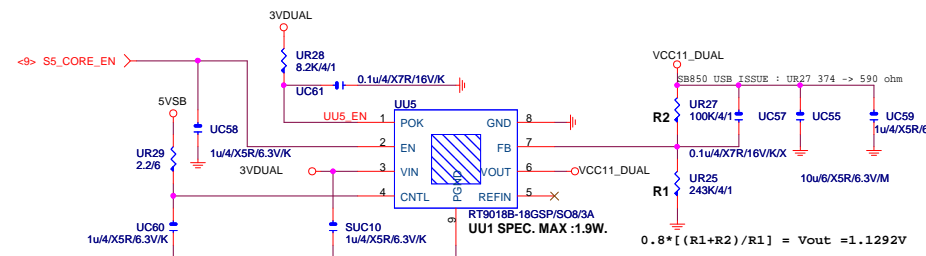
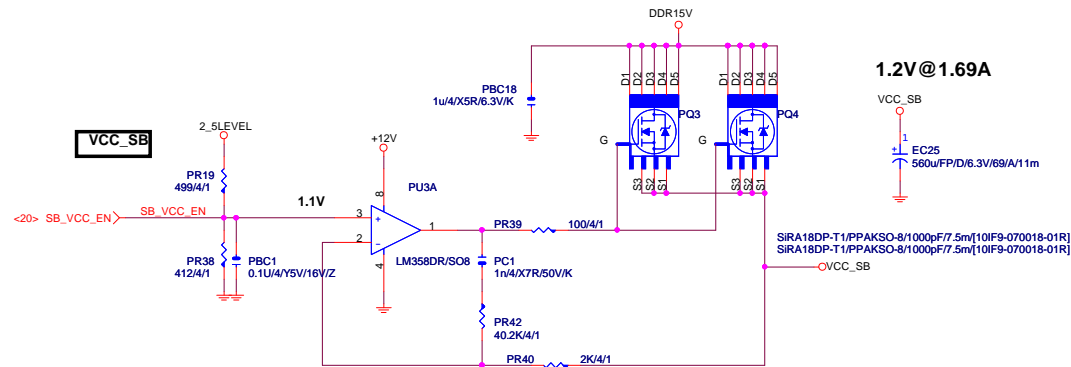
of

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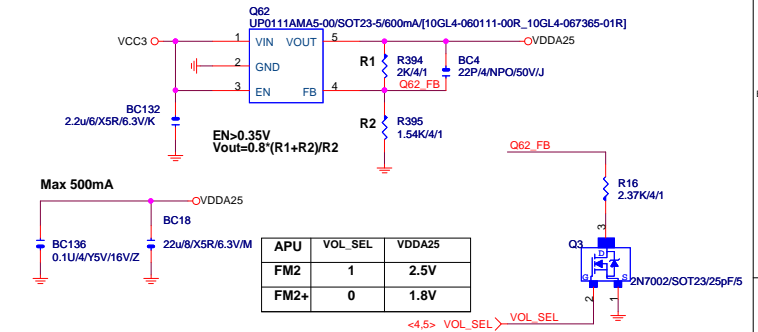
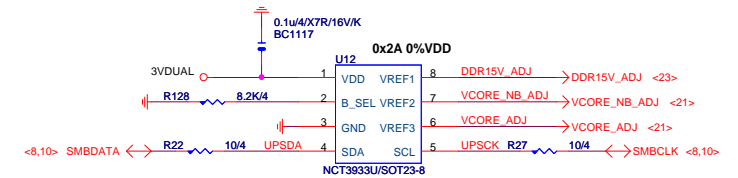
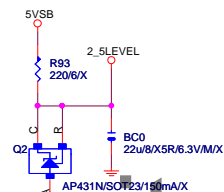
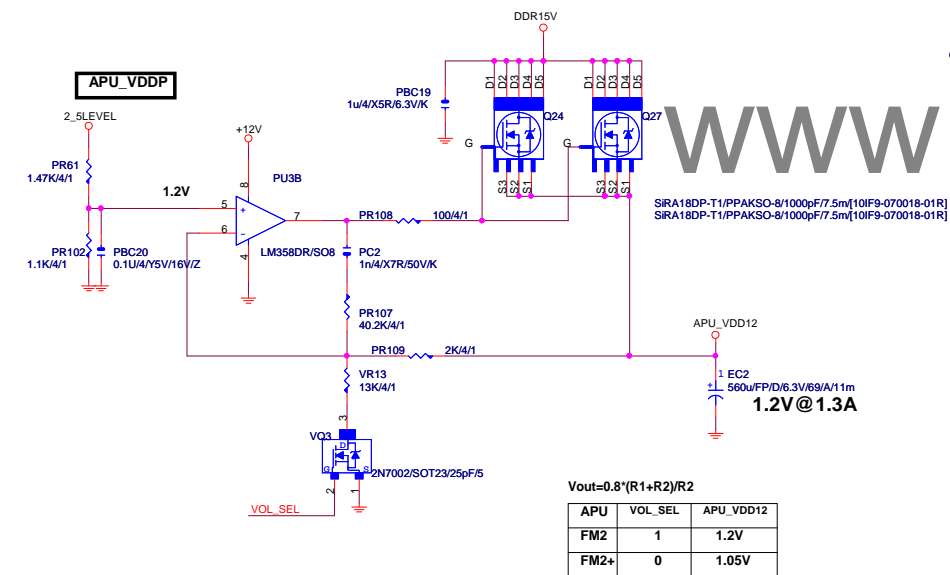








【技術通報R&D技術通報156】  
RT9018 (RICHTER) 與 NCT3730 (NUVOTON),  
EM5103GE (EMC) 做共用, 針對 PIN7 (FB) 分壓阻值部份  
(R1/R2) 須做修改為 100K 以上電阻值



**GIGABYTE**

Title  
**VCC\_SB, APU\_VDD12, VCC11\_DUAL, VDDA25**

Size  
Custom

Document Number  
**GA-F2A58M-DS2H**

Rev  
**3.0**

Date: Thursday, April 17, 2014

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